

10091783, 052002

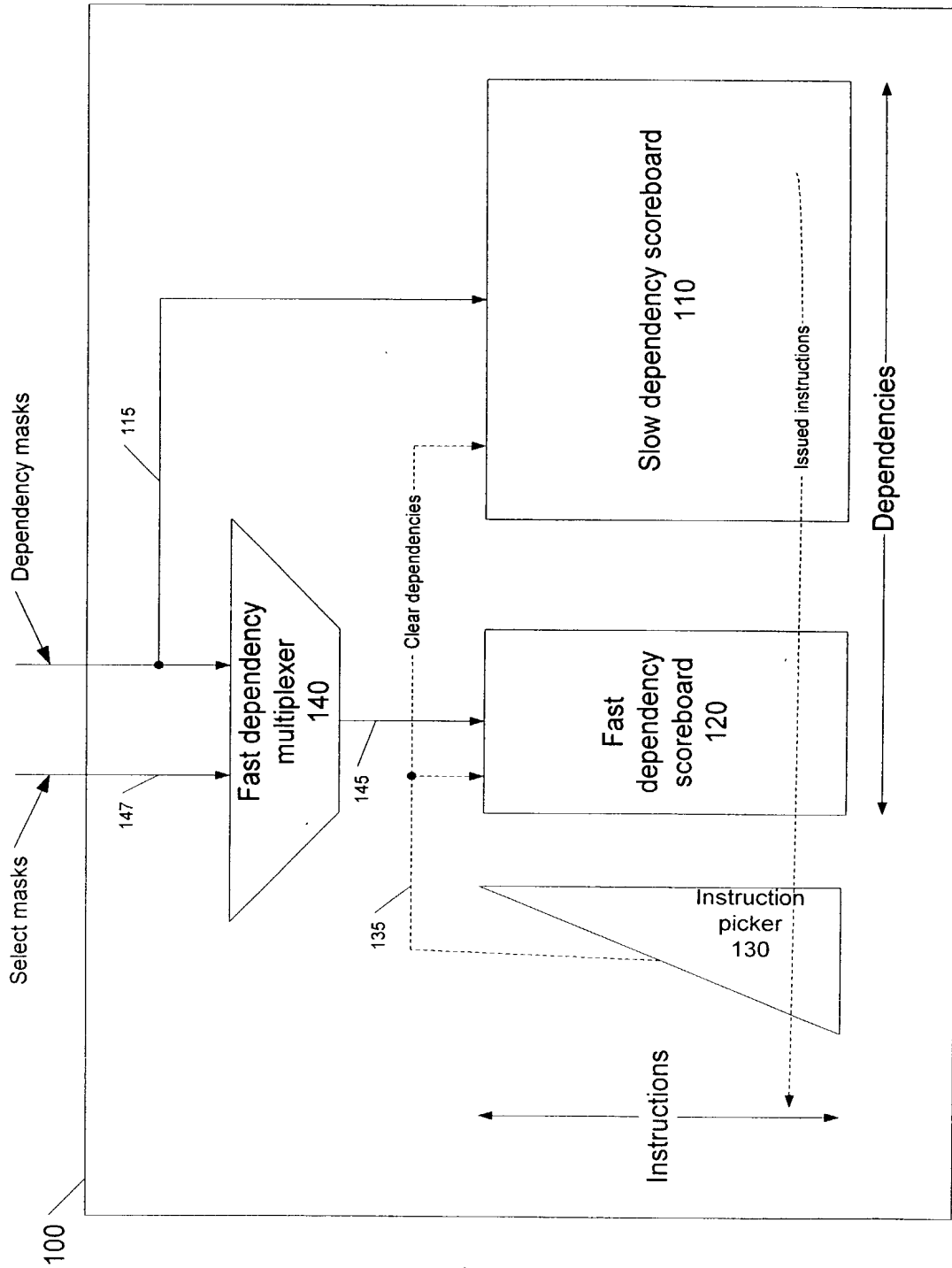


Fig. 1

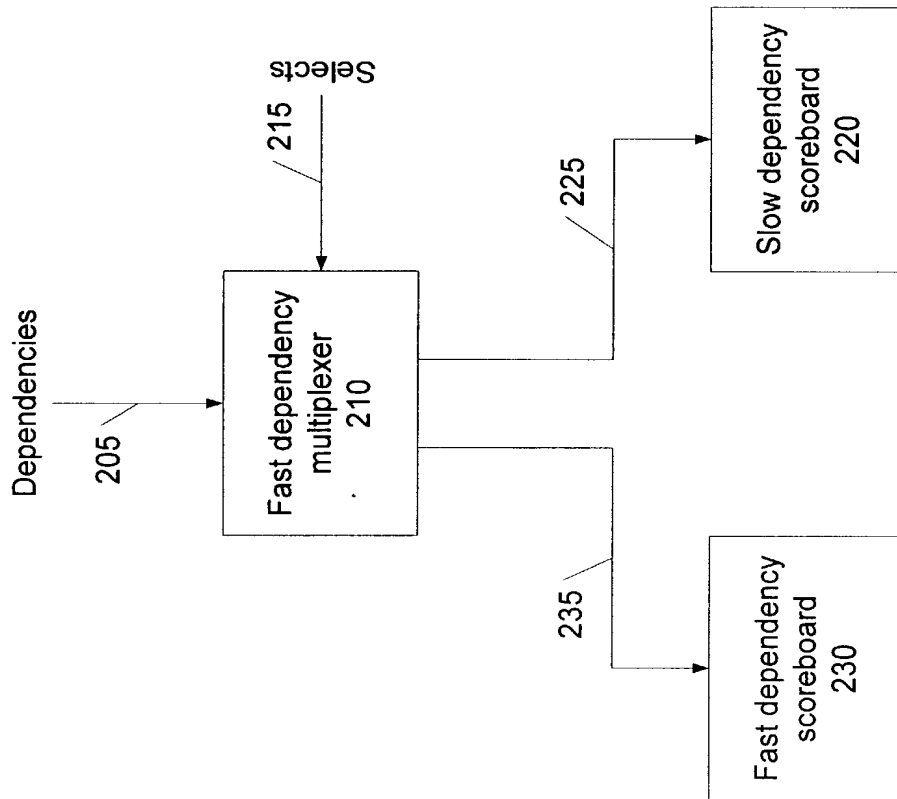


Fig. 2A



300

Instruction IDs of the first
instruction of each octet

128 bits

Octet	IID	127:120	119:112	111:104	103:96	95:88	87:80	79:72	71:64	63:56	55:48	47:40	39:32	31:24	23:16	15:8	7:0
0	0	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
1	8	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111
2	16	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111
3	24	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111
4	32	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111
5	40	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000
6	48	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000
7	56	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000
8	64	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000
9	72	00000000	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000
10	80	00000000	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000
11	88	00000000	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000
12	96	00000000	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
13	104	00000000	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
14	112	00000000	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
15	120	00000000	11111111	11111111	11111111	11111111	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000

Fig. 3A

R0	iid56
R1	iid57
R2	iid58
R3	iid59
R4	iid60
R5	iid61
R6	iid62
R7	iid63

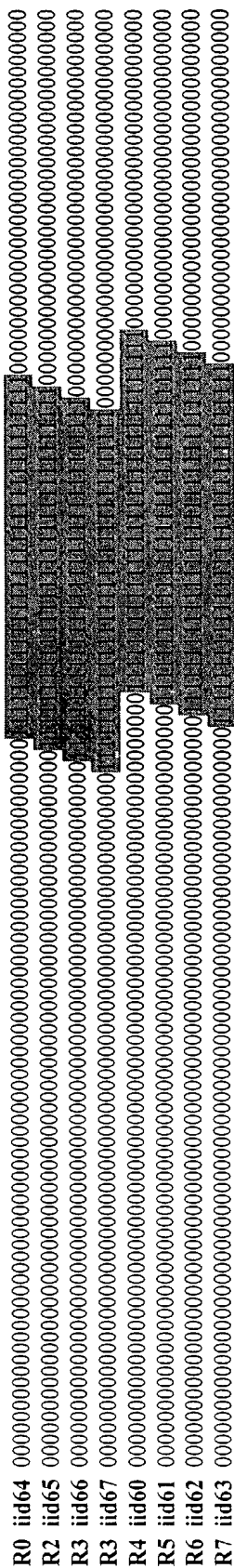
Write Pointer = 60 “Next Octet Pattern”

[illegible]

Fig 3B

[illegible]

FAST INSTRUCTION DEPENDENCY MULTIPLEXER



Single strand mode

Write Pointer = 60 “Final Select Pattern”

Fig 3C

